

Declaration and Power of Attorney For Patent Application

特許出願宣言書及び委任状

Japanese Language Declaration

日本語宣言書

下記の氏名の発明者として、私は以下の通り宣言します。

As a below named inventor, I hereby declare that:

私の住所、私書箱、国籍は下記の私の氏名の後に記載された通りです。

My residence, post office address and citizenship are as stated next to my name.

下記の名称の発明に関して請求範囲に記載され、特許出願している発明内容について、私が最初かつ唯一の発明者（下記の氏名が一つの場合）もしくは最初かつ共同発明者であると（下記の名称が複数の場合）信じています。

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled
PATH TRANSISTOR CIRCUIT AND METHOD FOR DESIGNING THE SAME, A DEVICE AND METHOD FOR OPTIMIZING LOGIC CIRCUITS, AND A COMPUTER PRODUCT

上記発明の明細書（下記の欄でx印がついていない場合は、本表に添付）は、

the specification of which is attached hereto unless the following box is checked:

___月___日に提出され、米国出願番号または特許協定条約国際出願番号を___とし、
 （該当する場合）___に訂正されました。

☐ was filed on _____
 as United States Application Number or
 PCT International Application Number
 _____ and was amended on
 _____ (if applicable).

私は、特許請求範囲を含む上記訂正後の明細書を検討し、内容を理解していることをここに表明します。

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

私は、連邦規則第37条第1条5.6項に定義されるとおり、特許資格の有無について重要な情報を開示する義務があることを認めます。

I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56.

Japanese Language Declaration (日本語宣言書)

私は、米国法典第35編119条(a)-(d)項又は365条(b)項に基づき下記の、外国以外の国の少なくとも一ヶ国を指定している特許協力条約365条(a)項に基づき国際出願、又は外国での特許出願もしくは発明者証の出願についての外国優先権をここに主張するとともに、優先権を主張している、本出願の前に出願された特許または発明者証の外国出願を以下に、括弧をマークすることで、示しています。

Prior Foreign Application(s)

外国での先行出願
 2000-180393

Japan

(Number)
 (番号)

(Country)
 (国名)

(Number)
 (番号)

(Country)
 (国名)

I hereby claim foreign priority under Title 35, United States Code, Section 119(a)-(d) or 365(b) of any foreign application(s) for patent or inventor's certificate, or 365(a) of any PCT international application which designated at least one country other than the United States, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or PCT international application having a filing date before that of the application on which priority is claimed.

Priority Not Claimed

優先権主張なし

15/June/2000

(Day/Month/Year Filed)
 (出願年月日)

(Day/Month/Year Filed)
 (出願年月日)

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私は、第35編米国法典119条(e)項に基づいて下記の米国特許出願規定に記載された権利をここに主張いたします。

(Application No.)
 (出願番号)

(Filing Date)
 (出願日)

(Application No.)
 (出願番号)

(Filing Date)
 (出願日)

私は、下記の米国法典第35編120条に基づいて下記の米国特許出願に記載された権利、又は米国を指定している特許協力条約365条(c)に基づき権利をここに主張します。また、本出願の各請求範囲の内容が米国法典第35編112条第1項又は特許協力条約で規定された方法で先行する米国特許出願に開示されていない限り、その先行米国出願書提出日以降で本出願書の日本国内または特許協力条約国際提出日まで期間中に入手された、連邦規則法典第37編1条56項で定義された特許資格の有無に関する重要な情報について開示義務があることを認識しています。

(Application No.)
 (出願番号)

(Filing Date)
 (出願日)

(Application No.)
 (出願番号)

(Filing Date)
 (出願日)

(Status: Patented, Pending, Abandoned)
 (現況: 特許許可済、係属中、放棄済)

(Status: Patented, Pending, Abandoned)
 (現況: 特許許可済、係属中、放棄済)

私は、私自身の知識に基づいて本宣言書中で私が行なう表明が真実であり、かつ私の入手した情報と私の信じることに基づく表明が全て真実であると信じていること、さらに故意になされた虚偽の表明及びそれと同等の行為は米国法典第18編第1001条に基づき、罰金または拘禁、もしくはその両方により処罰されること、そしてそのような故意による虚偽の表明を行なえば、出願した、又は既に許可された特許の有効性が失われることを認識し、よってここに上記のごとく宣誓を致します。

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Japanese Language Declaration (日本語宣言書)

委任状: 私は下記の発明者として、本出願に関する一切の手続きを特許庁商標局に対して遂行する手続または代理人として、下記の者を指名いたします。(弁護士、または代理人の氏名及び登録番号を明記のこと)

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith (list name and registration number)

特許代理人

And I hereby appoint as principal attorneys: David T. Nikaide, Reg. No. 22,663; Charles M. Marmelstein, Reg. No. 25,895; George E. Oram, Jr., Reg. No. 27,931; Robert B. Murray, Reg. No. 22,980; E. Marcie Emas, Reg. No. 32,131; Douglas H. Goldhush, Reg. No. 33,125; Monica Chin Kitts, Reg. No. 36,105; Richard J. Berman, Reg. No. 39,107; King L. Wong, Reg. No. 37,500; Karen K. Costantino, Reg. No. 35,107; James A. Poulos, III, Reg. No. 31,714; Patrick D. Muir, Reg. No. 37,403; Sharon N. Klesner, Reg. No. 36,335; and Murat Ozgu, Reg. No. 44,275; Bradley D. Goldizen, Reg. No. 43,637; and N. Alexander Nolte, Reg. No. 45,689.

直接電話連絡先: (名前及び電話番号)

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唯一または第一発明者名		Full name of sole or first inventor	
Mitsuru Sasaki			
発明者の署名	日付	Inventor's signature	Date
<i>Mitsuru Sasaki</i>	Nov. 20, 2000		
住所	Residence		
Kawasaki, Japan			
国籍	Citizenship		
Japanese			
私書箱	Post Office Address		
c/o FUJITSU LIMITED			
1-1, Kamikodanaka 4-chome,			
Nakahara-ku, Kawasaki-shi,			
Kanagawa 211-8588 Japan			
第二共同発明者名		Full name of second joint inventor, if any	
第二共同発明者の署名	日付	Second inventor's signature	Date
住所	Residence		
国籍	Citizenship		
私書箱	Post Office Address		

(第三以降の共同発明者についても同様に記載し、署名をすること)

(Supply similar information and signature for third and subsequent joint inventors.)

Docket No:

U.S. ASSIGNMENT

IN CONSIDERATION of the sum of One Dollar (\$1.00), and of other good and valuable consideration paid to the undersigned inventors (hereinafter ASSIGNOR) by:

FUJITSU LIMITED

1-1, Kamikōdanakā 4-chōme, Nakahara-ku, Kawasaki-shi,
Kanagawa 211-8588 Japan

(hereinafter ASSIGNEE), the receipt of which is hereby acknowledged, the undersigned ASSIGNOR hereby sells, assigns and transfers to ASSIGNEE the entire and exclusive right, title and interest to the invention entitled:

PATH TRANSISTOR CIRCUIT AND METHOD FOR DESIGNING THE SAME,
A DEVICE AND METHOD FOR OPTIMIZING LOGIC CIRCUITS, AND
A COMPUTER PRODUCT

for which application for Letters Patent of the United States was filed on _____,
and assigned Serial No. _____ (Arent Fox Kintner Plotkin & Kahn, PLLC
is hereby authorized to insert the series code, serial number and/or filing date hereon, when known)
and all Letters Patent of the United States to be obtained therefor on said application or any
continuation, division, renewal, substitute, reissue or reexamination thereof for the full term or terms
for which the same may be granted.

The ASSIGNOR agrees to execute all papers necessary in connection with application and any
continuing, divisional, reissue or reexamination applications thereof and also to execute separate
assignments in connection with such applications as the ASSIGNEE may deem necessary or expedient.

The ASSIGNOR agrees to execute all papers necessary in connection with any interference,
litigation, or other legal proceeding which may be declared concerning this application or any
continuation, division, reissue or reexamination thereof or Letters Patent or reissue patent issued
thereon and to cooperate with the ASSIGNEE in every way possible in obtaining and producing
evidence and proceeding with such interference, litigation, or other legal proceeding.

IN WITNESS WHEREOF, the undersigned inventors have affixed their signatures.

Mitsuru Sasaki	<u>Mitsuru Sasaki</u>	Nov. 20, 2000
	(SIGNATURE)	(DATE)
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	(SIGNATURE)	(DATE)
	(SIGNATURE)	(DATE)

NO LEGALIZATION REQUIRED

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